CLAIMS

What is claimed is:

1. A computer system comprising:

initialization memory containing initialization code,

a processor coupled to said initialization memory for executing said initialization code,

a static random access memory coupled to said processor for use in executing said initialization code.

2. The apparatus of Claim 1 wherein said static random access memory is assigned addresses overlaying a portion of the addresses assigned to said initialization memory.

3. The apparatus of Claim 2 further including logic for selecting the initialization memory when the processor needs to read initialization code and for selecting the static random access memory when the processor needs to read or write to random access memory.

4. The apparatus of Claim 1 further including dynamic random access memory coupled to said processor, wherein said initialization code is for initializing said dynamic random access memory.

5. The apparatus of Claim 4 wherein said processor uses primarily only said dynamic random access memory when executing application code.

6. A method for operating a computer system comprising;

providing initialization software in a initialization memory coupled to a processor,

providing static random access memory coupled to the processor,

executing the initialization code in the processor while using the static random access memory to store and retrieve variables needed by the code.

7. The method of Claim 6 wherein said computer system includes dynamic random access memory and said initialization code is for initializing said dynamic random access memory.

8. The method of Claim 7 further including using primarily only said dynamic random access memory when executing application code in said processor.

9. A computer system comprising:

dynamic random access memory,

initialization memory containing initialization code for initializing the dynamic random access memory at system startup, and

a static random access memory functional at system startup.

10. The system of Claim 9, further including:

a processor coupled to said initialization memory for executing said initialization code upon system startup and coupled to said static random access memory for use in executing said code.

11. The system of Claim 10 wherein said processor is coupled to said static random access memory after system startup for use in executing system code other than said initialization code.

12. The system of Claim 9 wherein said static random access memory is assigned addresses overlaying a portion of the address space assigned to said initialization memory.

13. The system of Claim 12 further including means for selecting said static random access memory when said processor is executing said initialization code.

14. A computer system comprising:

dynamic random access memory,

initialization memory containing initialization code for initializing the dynamic random access memory at system startup,

a processor coupled to said initialization memory for executing said initialization code,

a static random access memory coupled to said processor for use in executing said initialization code, said static random access memory connected to and powered by a system power supply which remains active whenever AC power is supplied to the computer system.